

JC971 U.S. PTO
 09/829838

 04/10/01

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. <i>DK</i> SCANNED <i>AC</i> Q.A. <i>JHT</i>	PATENT DATE
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APPLICATION NO. 09/829838	CONT/PRIOR	CLASS 712 377	SUBCLASS 1565	ART UNIT 2183 2816	EXAMINER Xo Bourne
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APPLICANTS

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TITLE

Digital phase locked loop

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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ISSUE FEE				
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Primary Examiner) (Date)		Amount Due	Date Paid
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